

Amendments to the Claims:

This listing of claims replaces all prior versions and listings of claims in the application:

Listing of Claims:

1. (Currently Amended) A storage system comprising:
 - a port functioning as an interface to a host;
 - a cache memory;
 - a shared memory;
 - a control device connected to said port, said cache memory, and said shared memory through a connection line; and
 - a-one or more disk devices connected to said control device, said disk devices comprising a plurality of physical devices, and whereby at least one logical device is provided for said host;
 - wherein said storage system is coupled to a service terminal connected thereto for receiving first priority information on each logical device provided for said host;
 - wherein said control device maps more physical devices to a logical device with a high priority than that with a low priority based on said first priority information received from said service terminal; and

wherein in the event of a failure, said control device performs control such that data held in said cache memory and belonging to ~~a~~each logical device is saved to ~~plural~~the physical devices mapped to each said logical device.

2. (Currently Amended) The storage system as claimed in claim 1, wherein said shared memory stores a mapping relationship between each logical device and ~~a~~ ~~plurality of~~the physical devices, said mapping relationship being established based on said first priority information.

3. (Original) The storage system as claimed in claim 1, wherein:
said service terminal is adapted to receive second priority information indicating a task priority; and
in the event of a failure, said control device performs control such that a job with a high priority is executed after it is dequeued from a priority queue within said storage system based on said second priority information received from said service terminal.

4. (Original) The storage system as claimed in claim 3, wherein said shared memory stores said second priority information received by said service terminal.

5. (Original) The storage system as claimed in claim 1, wherein if data is lost in the event of a failure, said storage system outputs information indicating a logical device

and a position in said logical device corresponding to said lost data to said service terminal.

6. (Currently Amended) A method for controlling a storage system which includes:

a port functioning as an interface to a host;

a cache memory;

a shared memory;

a control device connected to said port, said cache memory, and said shared memory through a connection line; and

a one or more disk devices connected to said control device, said disk devices comprising a plurality of physical devices, and whereby at least one logical device is provided for said host;

wherein said method comprises:

a first step of receiving first priority information on each logical device provided for said host; and

a second step of mapping more of said physical devices to a logical device with a high priority than that with a low priority based on said first priority information received at said first step, and in the event of a failure, saving data held in said cache memory and belonging to a each logical device to a plurality of the physical devices mapped to each said logical device.

7. (Currently Amended)) The method as claimed in claim 6, further comprising:
a third step of storing a mapping relationship between each logical device and a
plurality of the physical devices into said shared memory, said mapping relationship
being established based on said first priority information received at said first step.

8. (Original) The method as claimed in claim 6, further comprising:
a fourth step of receiving second priority information indicating a task priority;
and
a fifth step of, in the event of a failure, performing control such that a job with
a high priority is executed after it is dequeued from a priority queue within said
storage system based on said second priority information received at said fourth
step.

9. (Original) The method as claimed in claim 8, further comprising:
a sixth step of storing said second priority information received at said fourth
step, into said shared memory.

10. (New) A method for controlling a storage system which includes:
at least one port functioning as an interface to at least one host;
at least one cache memory;
at least one shared memory;

at least one control device connected to said at least one port, said at least one cache memory, and said at least one shared memory through at least one connection line; and

one or more disk devices connected to said at least one control device, said disk devices comprising a plurality of physical devices, and whereby at least one logical device is provided for the at least one host;

wherein said method comprises:

a first step of receiving first priority information on each logical device provided for said at least one host;

a second step of mapping more of said physical devices to a logical device with a high priority than that with a low priority based on said first priority information received at said first step, and in the event of a failure, saving data held in said at least one cache memory and belonging to each said logical device to the physical devices mapped to each said logical device;

a third step of storing a mapping relationship between each logical device and the physical devices into said at least one shared memory, said mapping relationship being established based on said first priority information received at said first step;

a fourth step of receiving second priority information indicating a task priority; and

a fifth step of, in the event of a failure, performing control such that a job with a high priority is executed after it is dequeued from a priority queue within said

storage system based on said second priority information received at said fourth step.

11. (New) The method as claimed in claim 10, further comprising:
 - a sixth step of storing said second priority information received at said fourth step, into said shared memory.